

In the Specification:

Please replace the paragraph on page 13, line 25 through page 14, line 8, with the following:

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A method according to the present invention may be better understood with reference to the flowchart shown in Figure 4. Figure 4 illustrates steps performed by the digital processor or DSP 12. Step 50 represents the start of the process. The DSP 12 receives data from the T1/E1 interface in step 51. In a preferred embodiment, the DSP 12 implements a G.711 codec mode optionally using a μ -Law PCM decoder or an A-Law PCM decoder. The DSP 12 may alternatively implement a G.723 codec mode, a G.729 codec mode, a G.726 codec mode, a G.727 codec mode, a G.728 codec mode, or other codecs as desired. The DSP 12 may be reconfigured by downloading software into the DSP 12 for substantially any desired codec. The DSP 12 also provides echo cancellation in step 53. An echo path delay from 8 milliseconds to 128 milliseconds may be selectively provided in 8 millisecond increments. The DSP 12 provides speech coding in step 54 (for data to be used when no DTMF signals are present). In the case where no DTMF signals are present, the encoded speech ~~to~~ is provided to the controller 13 in step 56 through a controller and DSP interface. The process loops back to step 51 as long as data is being received to be processed. When no more data is received, the DSP processing is completed in step 57.